

**Notice of References Cited**

Application/Control No.

09/786,560

Applicant(s)/Patent Under

Reexamination

UCHIZONO ET AL.

Examiner

Thomas J. Cleary

Art Unit

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	WO 9908196 A1	02-1999	World Intellect	SIDDAPPA, MAHESH	G06F 13/14
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Robert Stuart, "Implementing an IrDA Control Peripheral", Sharp IrDA Application Note, [3 February 1999].
	V	Universal Serial Bus Specification, Revision 1.0, [15 January 1996].
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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